

IN THE SPECIFICATION

Please amend the paragraph beginning at column 13, line 66 as follows:

In the clock regenerating circuit 2A, the identifying circuit 11 may be formed of plural identifying units corresponding to the number of plural signals obtained by demodulating the multilevel orthogonal modulated signal. The clock regenerating unit 15, the phase adjusting unit 16, and the clock phase detecting unit [14] 14A may be used in common to plural identifying units.